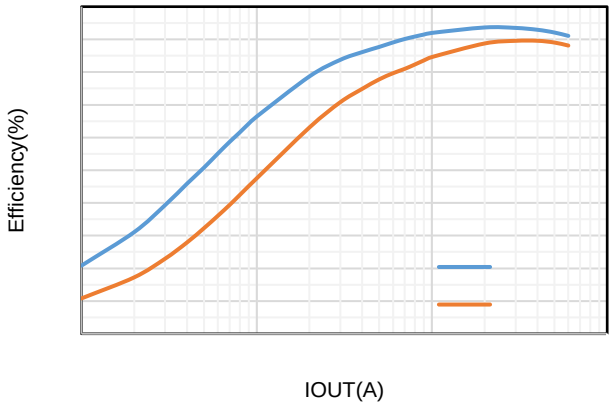
REVISION HISTORY

		[redacted]		
Current Limit				
Switching Frequency				
Soft Start Time				
Protection				

TYPICAL CHARACTERISTICS



FUNCTIONAL BLOCK DIAGRAM

Oscillator
with PLL

SCT

Thermal Shutdown

°

°

APPLICATION INFORMATION

Typical Application

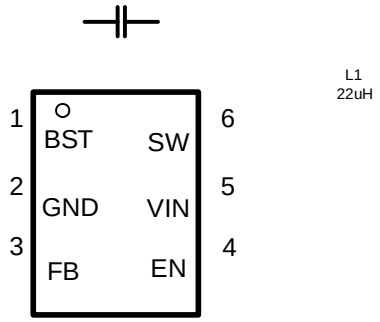


Figure 16. 24V Input, 5V/0.6A Output

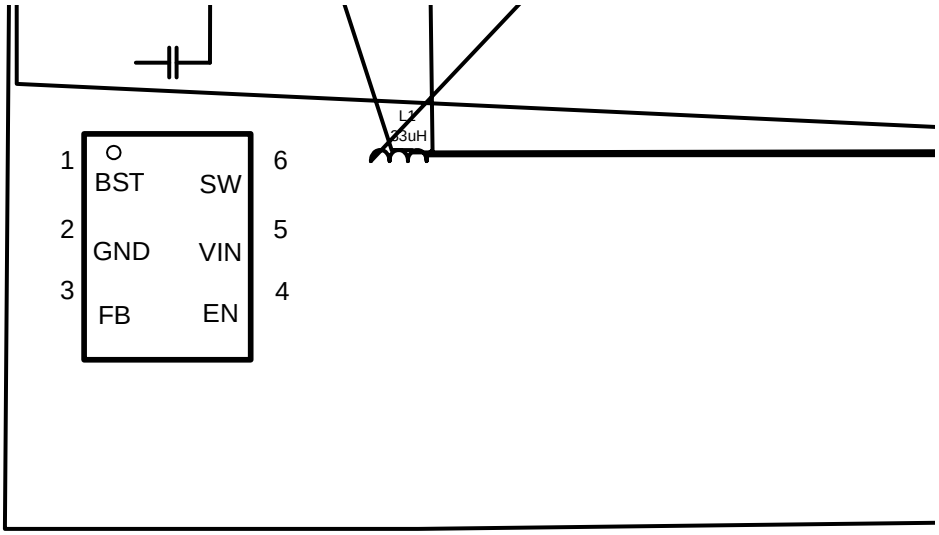


Figure 17. 24V Input, 12V/0.6A Output

Design Parameters

Design Parameters	Example Value
Input Voltage	24V
Output Current	0.6A
Switching Frequency	1.2MHz
Start Input Voltage (rising VIN)	5.5V
Stop Input Voltage (falling VIN)	5V

Output Voltage

Table 1. R_1, R_2Value for Common Output Voltage
(Room Temperature)

V_{OUT}	R_1	R_2

Bootstrap Capacitor Selection

Output Capacitor Selection

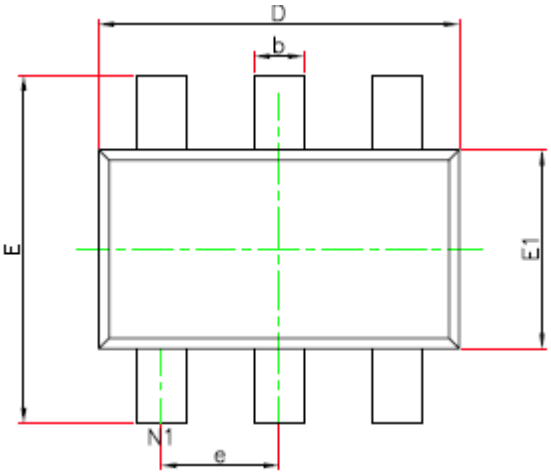
Application Waveforms

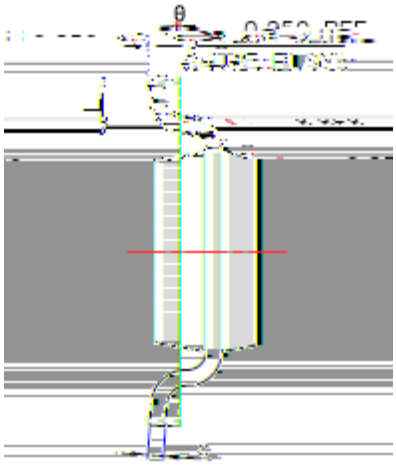
Layout Guideline

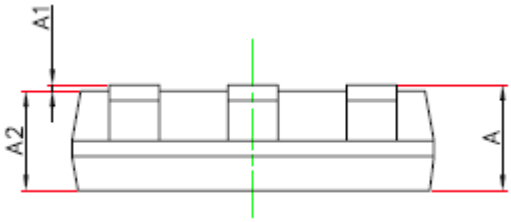
1.

2. 578178 Tc[(1.)]TE/Span <</MCID 15/Lang (en-US)>BDC q20.00000912 0 612 792 reW*nBT/F5 9.96 Tf1 0 0 1 309.05 673.66 Tr

PACKAGE INFORMATION







SYMBOL	Unit: Millimeter		
	MIN	TYP	MAX
	o		o

NOTE:

1.

2.

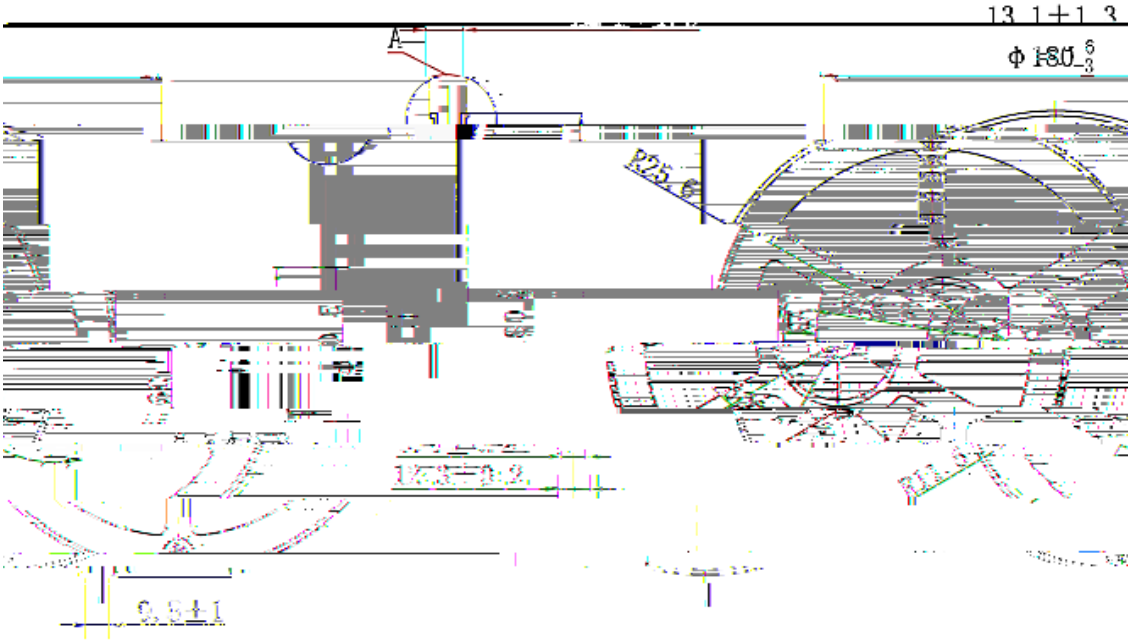
3.

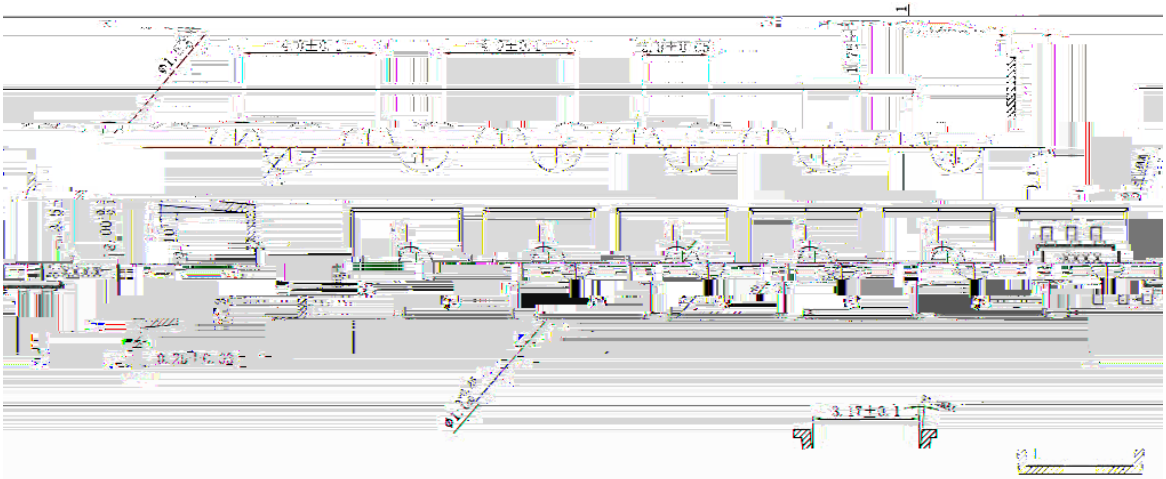
4.

5.

6.

TAPE AND REEL INFORMATION





Feeding Direction