



3.8V-28V Vin, 3A Synchronous Step-down DCDC Converter with EMI Reduction

FEATURES

- (0 , 5 H G X F W L R Q Z L W K 6 Z L W I F U K L I
- N + J 6 Z L W F K L Q J) U H T o % F o f u e n z y L
- Spread 6 S H F W U X P



SCT2330C

REVISION HISTORY

127(3DJH QXPEHUV IRU SUHYLRXV UHYLVLRQV PD\ GLIIHU IURP SDJH QXP
 5HYLVLRQURGXFWLRQ
 5HYLVLRQSGDWH GHVFULSWLRQ
 5HYLVLRQ SDBSGDJWH
 5HYLVLRQSGDWMLFH 2UGHU ,QIRUPDWLRQ

DEVICE ORDER INFORMATION

ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PACKAGE MARKING	PINS	PACKAGE DESCRIPTION
6 & 7 & 79%	7DSH 6H		&		7627 /

ABSOLUTE MAXIMUM RATINGS

Over operating free-air temperature unless otherwise noted⁽¹⁾

PIN CONFIGURATION

SW	5	Switching node of the buck converter.
BST	6	Power supply for the high-side power MOSFET gate driver. Must connect a 0.1uF or greater ceramic capacitor between BST pin and SW node.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	DEFINITION	MIN	MAX	UNIT
9.1	, Q SXW YROWDJH UDQJH			9
7-	2 SHUDWLQJWHPISIH UDWXUH UDQJH XQOHVV RWKHUZZLVH QRWHG			

TYPICAL CHARACTERISTICS

)LJXUH(IILFLHQF\ YV /RDG &XUUH(

)LJXUH(IILFLHQF\ YV /RDG &XUUH

OPERATION

Overview

7K1&7 &GHYLFH LW L9 SXW \$ RWLSHQKQ\ LQWHJUDWHG V\QFKZRH
GHYLFH HPSOR\V IL[HG IUHTXHQF\ SHDN FXUUHQW PRGH FRQWURO \$Q LQV

SCT2330C

RSHUDWLRQ LV GLVDEOHG ZKHQ (1 YROWWLSIOPDOIDEORZ LWV ORZHU WK
\$Q LQVX SXOO XS FXURQWFWURPFHQWHUQDO ('2 SLRZHU DQDLH W/BWLDW

)RU PRUH LQ [ZUPDLRQ QWHQW](#) 6E EFRQ & RQWHQW 7H \$QR5LRK&R5

3URGXFW)ROGHU /LQNV



SCT2330C

Bootstrap Voltage Regulator

\$Q H[W H U Q V D O D S R R D V S D F L W R U D E G I V 6 Z H S L Q \$ R Z H U V V I O G R H D S R Q H U K I O 2 6) (7 J D W H C
7 K H E R R W V W U D S F D S D F L W R U Y R O W D J H L V F K D U J H G I V R B H D Q R L Z Q H W H O 2 6) (7 H I C
R I I D Q G / L O G R Z S R Z H U 0 2 6) (7 L V R Q

7 K H I O R V D X V S L S O O \ % 6 7 8 W R 6 W K U H V K R O G L V 9 U L V L Q J D Q G K \ V R B M H V M H U R
R S H U D W H V Z L W K K S J K R Q R I Q Q V O F C H S R P R G H I R U F H U W X M Q P C R I Q M W H E M D O W K E J
E R R W V W U D S F D S D F L W R U L Y R V O R D J O R D W E R R N W H M S U D S H F D S D F L W R U V X I I L F L
E R R W V W U D S F D S D F L W R U 6 7 8 9 R 2 S R F E H Q R 6 7 7 K H 8 9 W H U Y H Q H V W R W X U Q R Q O R
S H U L R G L F D O O \ W R U H I U H V K W K H Y R O W D J H R I E R R Y H W V D V 0 D S H F 0 8 5 W F L W D R Q W H V F

Low Drop-out Regulation

7 R V X S S R U W W K H D S S O L F 0 W L H R I O H R I F M P E D O V O Z M R I Q W 0 B J H W D Q G 9 L Q W K H / R Z ' U
L P S O H P H Q W H G E \ 8 W : K H H 0 8 9 7 , 1 L V F O R V H W R R X W S X W Y R O W D U J H J D Q I G P L V Z L P X
R Q W L Z L H O E H H [W H Q G H G W R D Y R L G R X W S X W Y R O W D J H G U R S V V Z L W F K L
P D [L P X P V Q P H 7 W S L V W U L J J H O U C H 6 H 6 : Q Z P D [G X W \ F R S H H U D W S L R Q 7 K X V W K H
G X W \ F I F W K H R V Z L W F K L Q J U H J X O B X W R U ' 0 X R S I H Q U D M R I Z R O F S Q E H Y H U \ K L J K D V
' X U L Q J 0 8 2 V Y B O W D J H G L I I H U H Q F H R I L Q S X W D Q G R X W S X W Y R O W D J H V L H
W K H R X W S X W F D Q W U D Q N M Q R S X ' 2 F S H R U D V I O L R K D R G H

7 K H P L Q L P X P R S H U D W L Q D E B X W J H C F Q 0 D P I R W H R I I H F W L Y H O \ S U H Y H Q W D X G L R
E \ V Z L W F K L Q J I U H T X H Q F \ Z K H Q Z R U N L Q J Z L W K O D U J H G X W \ F \ F O H

Thermal Shutdown

2 Q F H W K H M X Q F W L R Q W 8 P S H U D W H X H G V I & Q W K H H W K H U P D O V H Q V L Q J F L U F X L
V Z L W F K L Q J D Q G U H V W D U W V Z L W K W K H M * X & Q F W K I H R U P D V C H F 8 M W I G W X Q H S L D H O C H I Q Q
R Q G H Y L F H G X U L Q J H [F H V V L Y H K H D W D Q G S R Z H U G L V V L S D W L R Q F R Q G L W L

APPLICATION INFORMATION

Typical Application

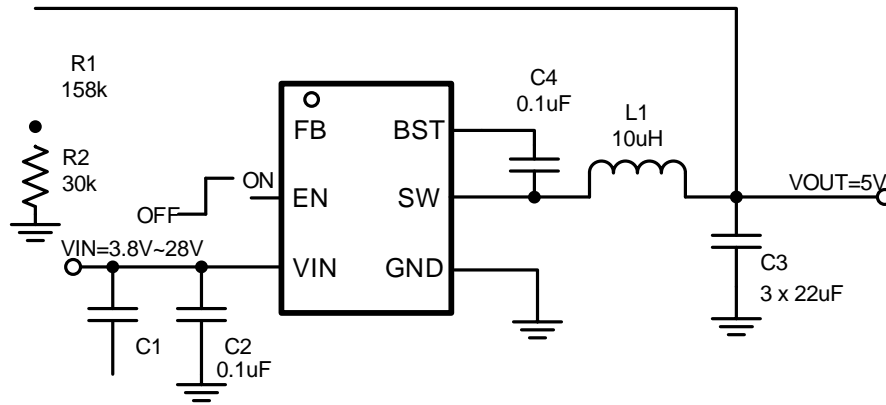


Figure 11. 24V Input, 5V/3A Output

Design Parameters

Design Parameters	Example Value
Input Voltage	24V
Output Voltage	5V
Output Current	3A
Output voltage ripple (peak to peak)	±8mV
Switching Frequency	400kHz

SCT2330C

SCT2330C

Output Feed-Forward Capacitor Selection

7KH&7 & KDV WKH LQWHUQDO LQWHJUDWHG ORRS FRPSHQVDWLRQ DV V
FRSHQVDWLRQ QHWDZREKRLQWVWRGHOYQGDSDFLWRU 8VXDOO\ WKH W\SH ,, F
KDV D SKDVH PDUJLQ EHWZHHQ DQG GHJUHH +R ZHRZH(65LIWKKHF R QW B
UHVXOWV LQ ORZ SKDVH PDUJLQ 7R LQFUHDVIR UZDWHU GFEQSKW\WGH W B KEDVRV V
SKDVH PDUJLQ DW WKRYFRUQIYUHUW(HQ FWRIRVQ LV XVHG WR UFZDDJFX Q D S\DF W

$$= \frac{1}{2 \cdot \times 1} \tag{10}$$

Output Feedback Resistor Divider Selection

7KH&7 & IHDWXUHV H[WHUQDO SURJUDPPDEOH RXWSXW YROWDJH E\ XVL
VKRZQ LQ WKH W\SLFDO DSSO\W\WLRQDWLWUFXLWWRJRDQFXODWH WKH UHV

$$1 = \frac{(\quad - \quad) \times 2}{\quad} \tag{11}$$



SCT2330C

Application Waveforms (Continued)

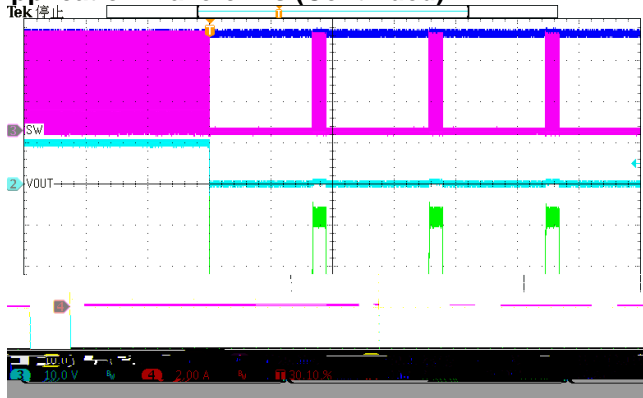


Figure 18. Over Current Protection(0A to hard short)

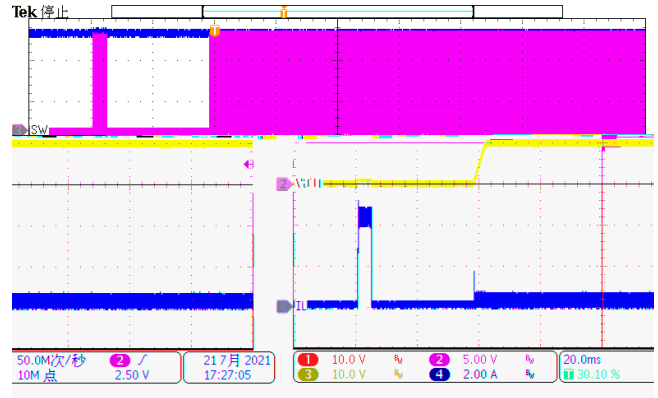


Figure 19. Over Current Release (hard short to 0A)

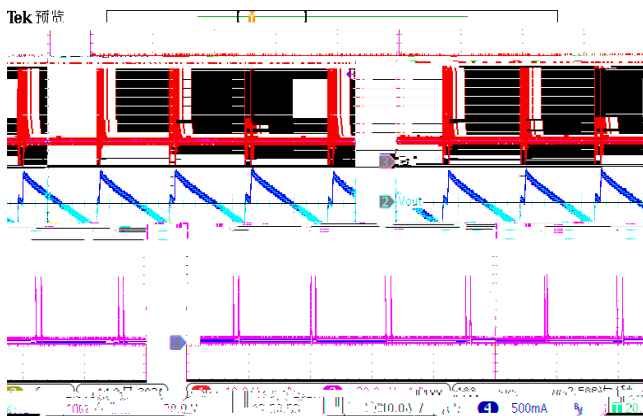


Figure 20. Output Ripple (Iload=10mA)

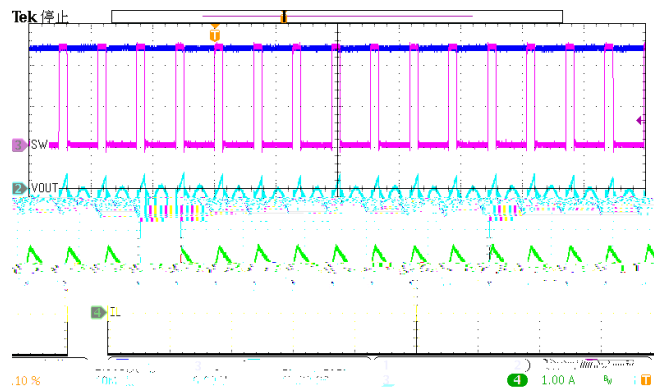


Figure 21. Output Ripple (Iload=1A)

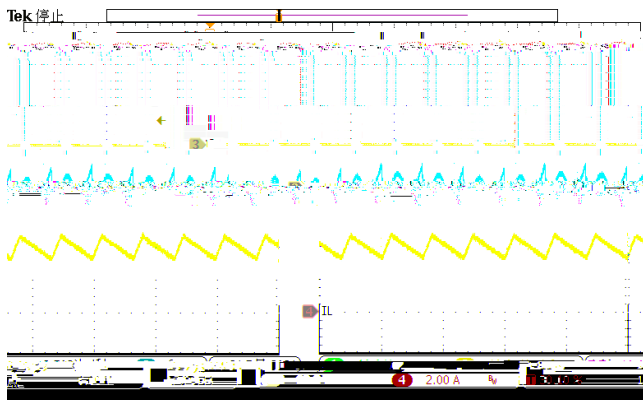


Figure 22. Output Ripple (Iload=3A)

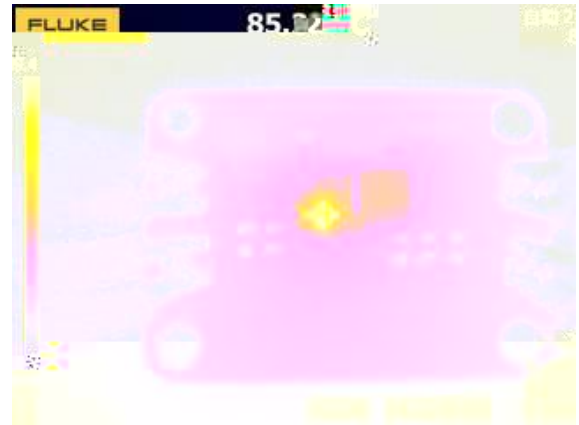
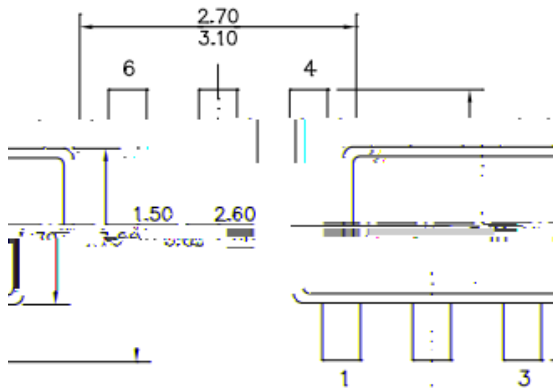


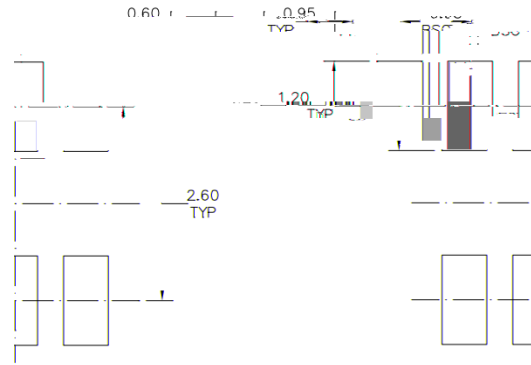
Figure 23. Thermal, 24VIN, 5Vout, 3A

SCT2330C

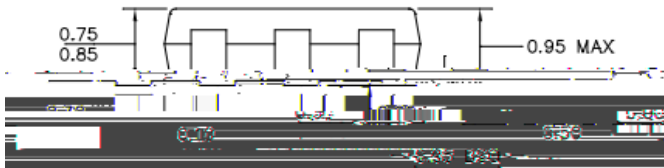
PACKAGE INFORMATION



7 2 3 9, (:



5 (& 200 (1' (' / \$1' 3 \$77 (51



6, '(9, (:

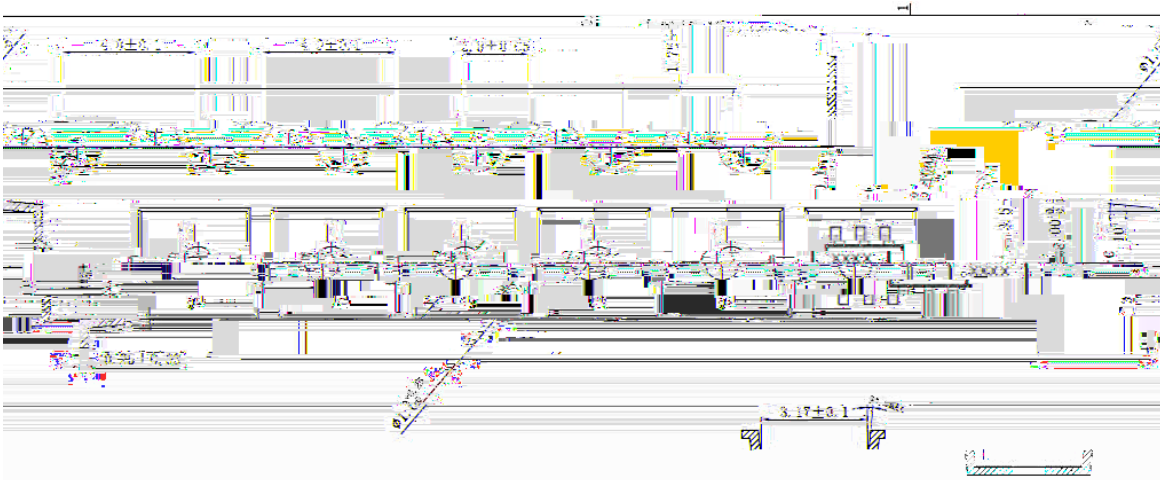
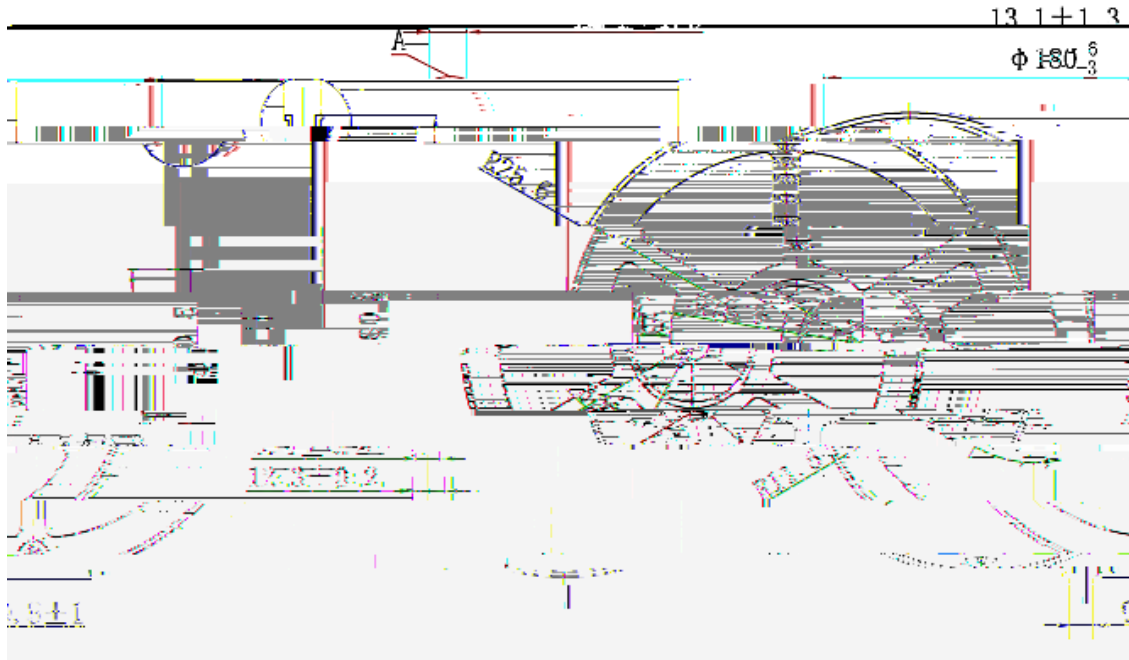
) 5 2 1 7 9, (:

' (7 \$, / \$

NOTE:

1. \$// ',0(16,216 \$5(,1 0, //,0(7(56
2. 3\$&.\$*/((1*7+ '2(6 127 ,1&/8'(02//)/\$6+ 3527586,21 25 *\$7(%855
3. 3\$&.\$*(:,'7+ '2(6 127 ,1&/8'(,17(5/('\$')/\$6+ 25 3527586,21
4. /('\$' &23/\$1\$5,7< %27720 2) /(\$'6 \$)7(5)250,1* 6+\$/// %(0, //,0(7(56 0\$;
5. '5\$:,1* &21)2506 72 -(('(& 02 9\$5,\$7,21 \$%
6. '5\$:,1* ,6 127 72 6&\$/(
7. 3,1 ,6 /2:(5 /()7 3,1 :+(1 5(\$',1* 723 0\$5.)520 /()7 72 5,*+7 6(((;\$03/(723 0\$5.

TAPE AND REEL INFORMATION



Feeding Direction

