

SCT2120

NAME	PIN NUMBER		PIN FUNCTION
	FCTQFN1.5X2-8L	FCSOT23-8L	
1:			1 /
2.		#	5 /
- : 0			-
B A			
: 0			
			9 21 B5
B5	#		9 21
			B5

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PARAMETER	DEFINITION	MIN	MAX	UNIT
B ₅	5			B
B _A				B
6				/

PARAMETER	DEFINITION	MIN	MAX	UNIT
B ₁₀	4 . 9 4.9 - : 56101/ 6			B
	/ 0 9 / 09 - : 56101/ 6			B
	4.9 / 09 - : 51 0- 6101/ 6			

PARAMETER	THERMAL METRIC	FCTQFN1.5X2-8L	UNIT
6	6	%	

2 1 8 / 2 - 2 1 8 / .

2 8 2 /

2 B₂ 2 AB8

(— 1)

&

B A
B 12
2.
2.

2. 2.

Under Voltage Lockout UVLO

/ A B 8 AB8 #B B5
B B5

Enable(EN)

1: B 2 1: / 1: B 9 8 1: 1:
9 21 .

Soft Start(SS)

/

Over Current Protection (OCP) and Hiccup Mode

5 9 21 - 22 : 9 21 9 21 9 21 : 9 21
9 21 9 21 9 21

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Output Voltage

7 A

1 (— 1) 2

&

B 12
B

V _{OUT}	R ₁	R ₂
B	7	7
B	7	7
B	7	7
B	7	7

Inductor Selection

9 0/ 0/ 8

4 -

5 1

(-)

&

5
8
2
A

9

#

5 1-7

5 9

$$\sqrt{\frac{()^2}{2} + \frac{1}{12} ()^2}$$

#

&

5 1-7

5 A 0/

5

5 9

9

5

- .

5 1-7

/

Input Capacitor Selection

- / 0/ 0/ 0/ 0/

D D#

0/

A

1

1

2

B5

: 0

1

-
9

$$\sqrt{\frac{1}{12} ()^2}$$

1

————— (1 —)

&

V_{IN}
2
/ 5
B A
B₅

2

2 D#

B

-

2

Output Capacitor Selection

1

1

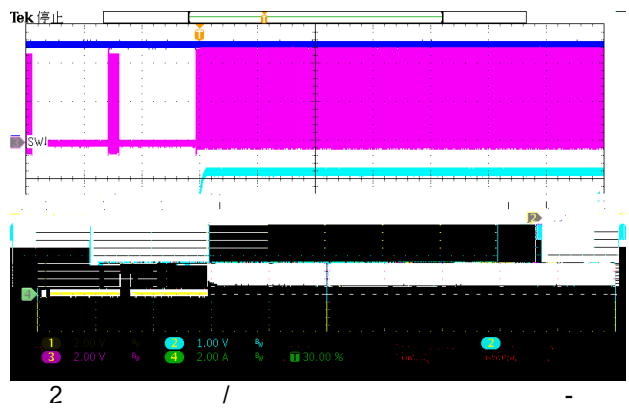
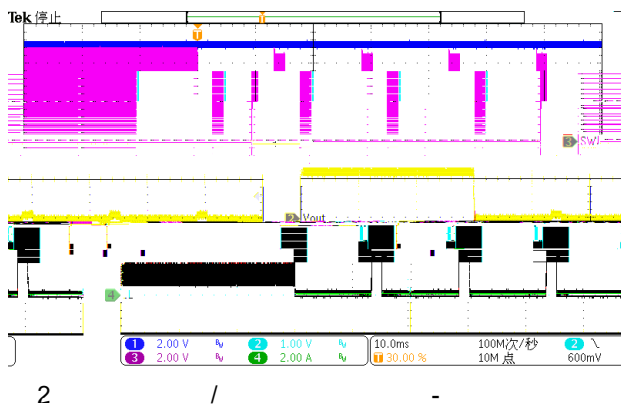
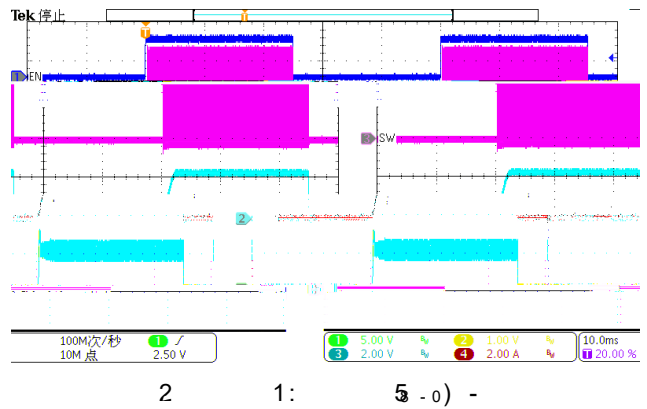
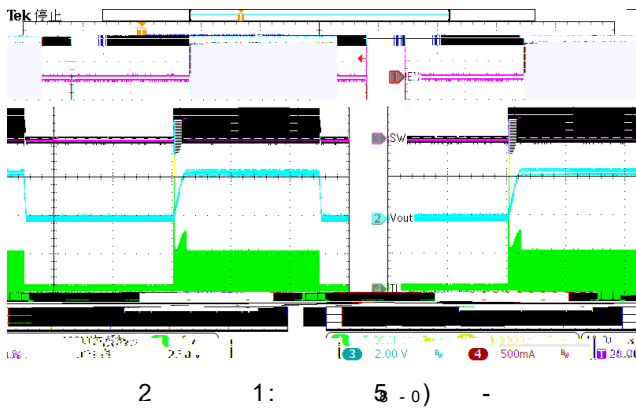
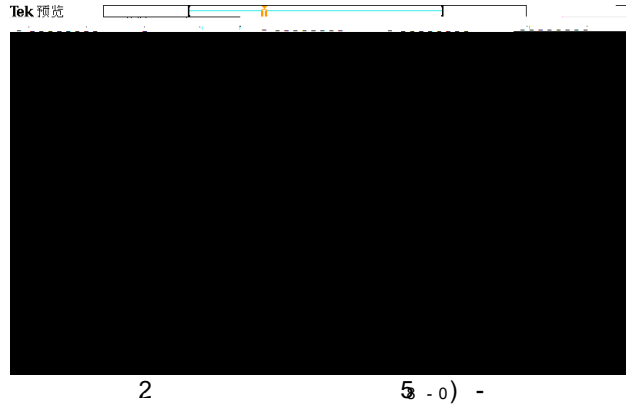
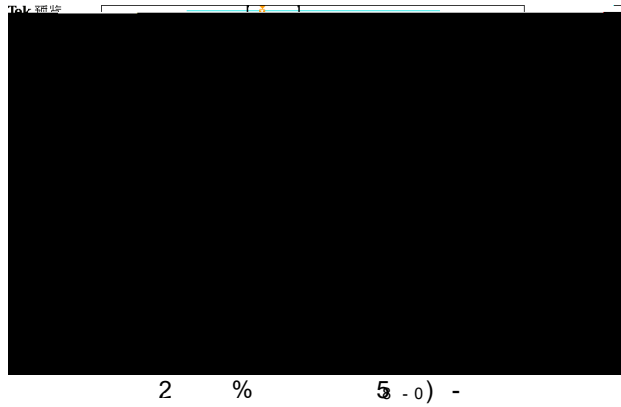
2

1

2

Application Waveforms

B5) B B A) B



Application Waveforms

2 8 - - - / 2 8 -

Layout Guideline

/ . / &
2
/ .
1 B5 9
2

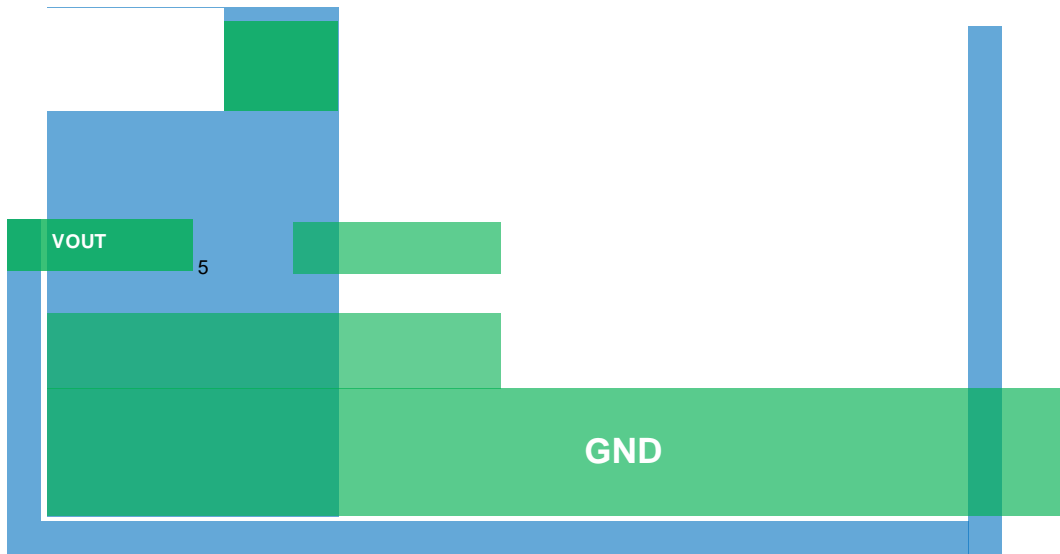
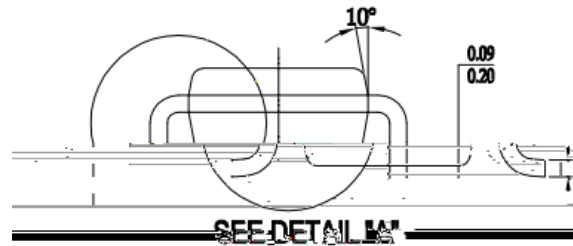
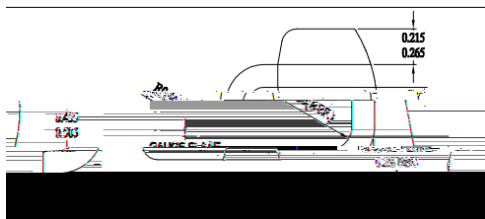
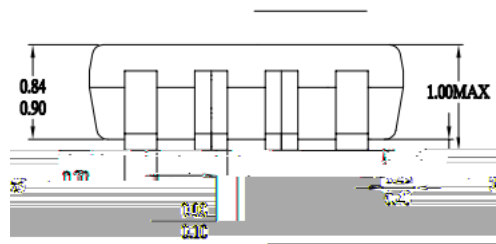
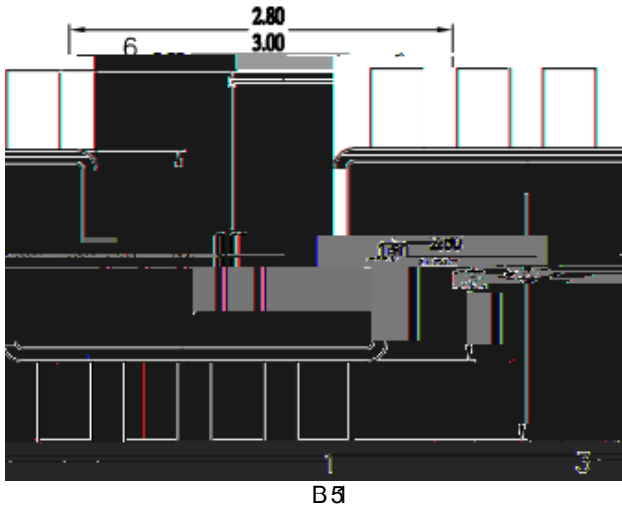


Figure 22. PCB Layout Example (SCT2120 TTBR)

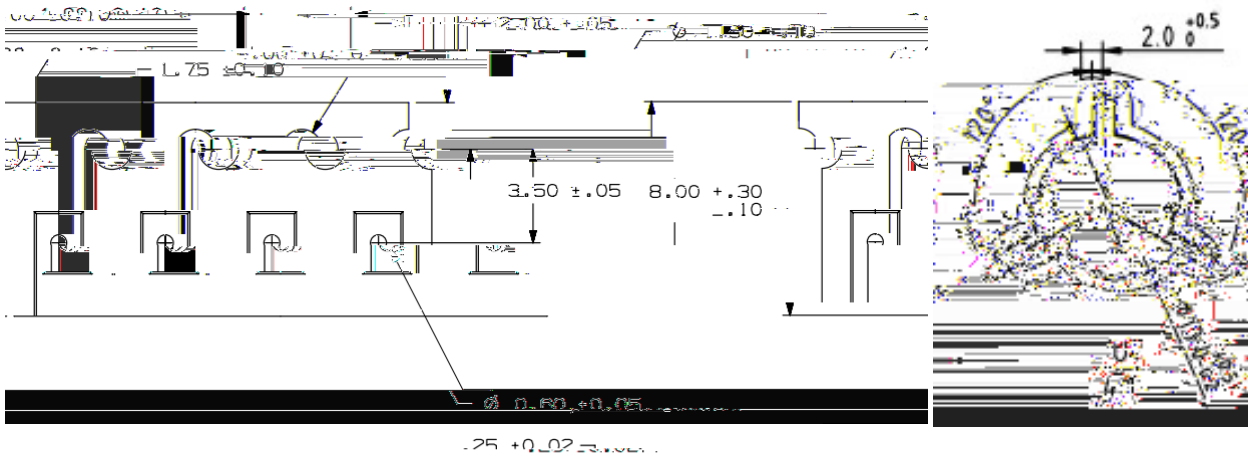
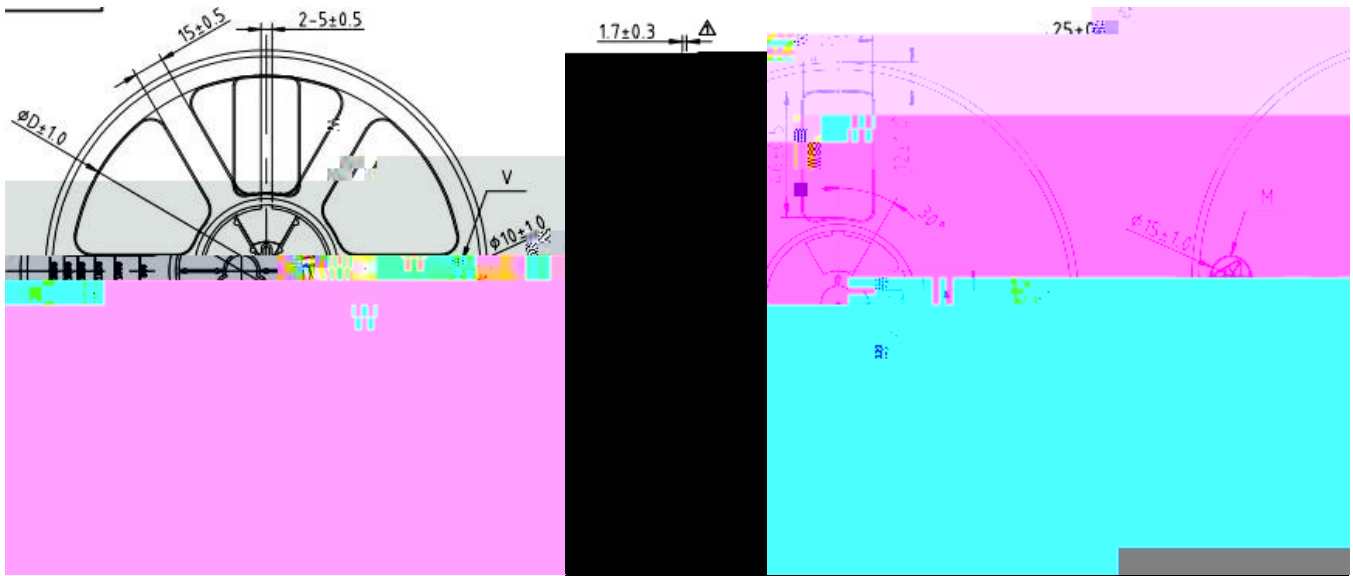
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2/ 8 0

NOTE:

81-0 2 - 91 / # 457: 1 #99
 81-0 25 54 801 8- 10
 . 4 - / 7- 1 81: 4 - : 0 5 4 0 : 5 / 8A01 28- 4
 2 910 81-0 4- 88.1 8- : - 54 1 1/ : 1 - : 41 5 45
 / : 885 0 91: 5 : 99
 A: 19 B10 28- 4.1 11: 81-0 - / 7- 11: 0 28- 4 4- 88
 : 1D/ 110 99 2 9 . 9 . 0E 1 51
 # 41 0 - 8 - 8E 2/ 1 5
 41 80 0A/ 54 A 5 91- : 80 0A/



NOTICE: